



FIG. 1

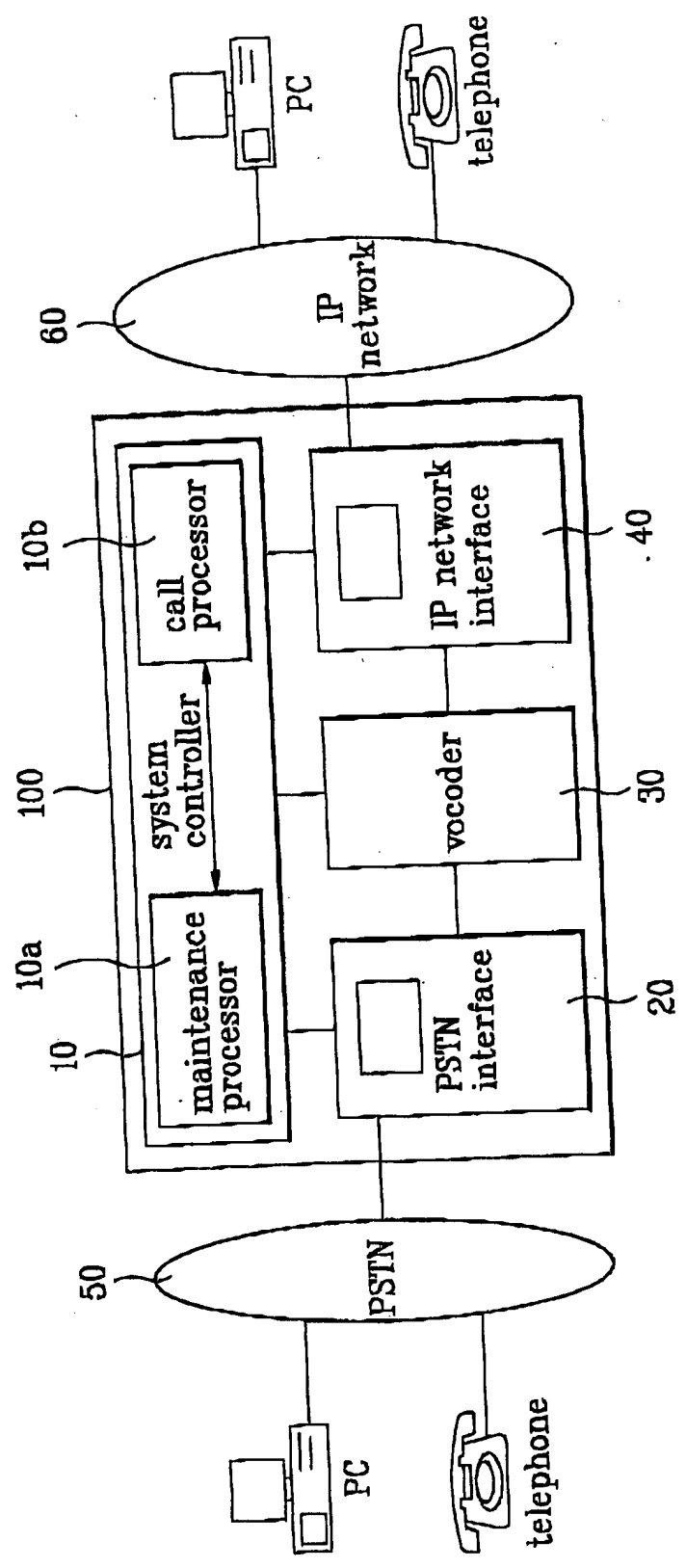


FIG. 2

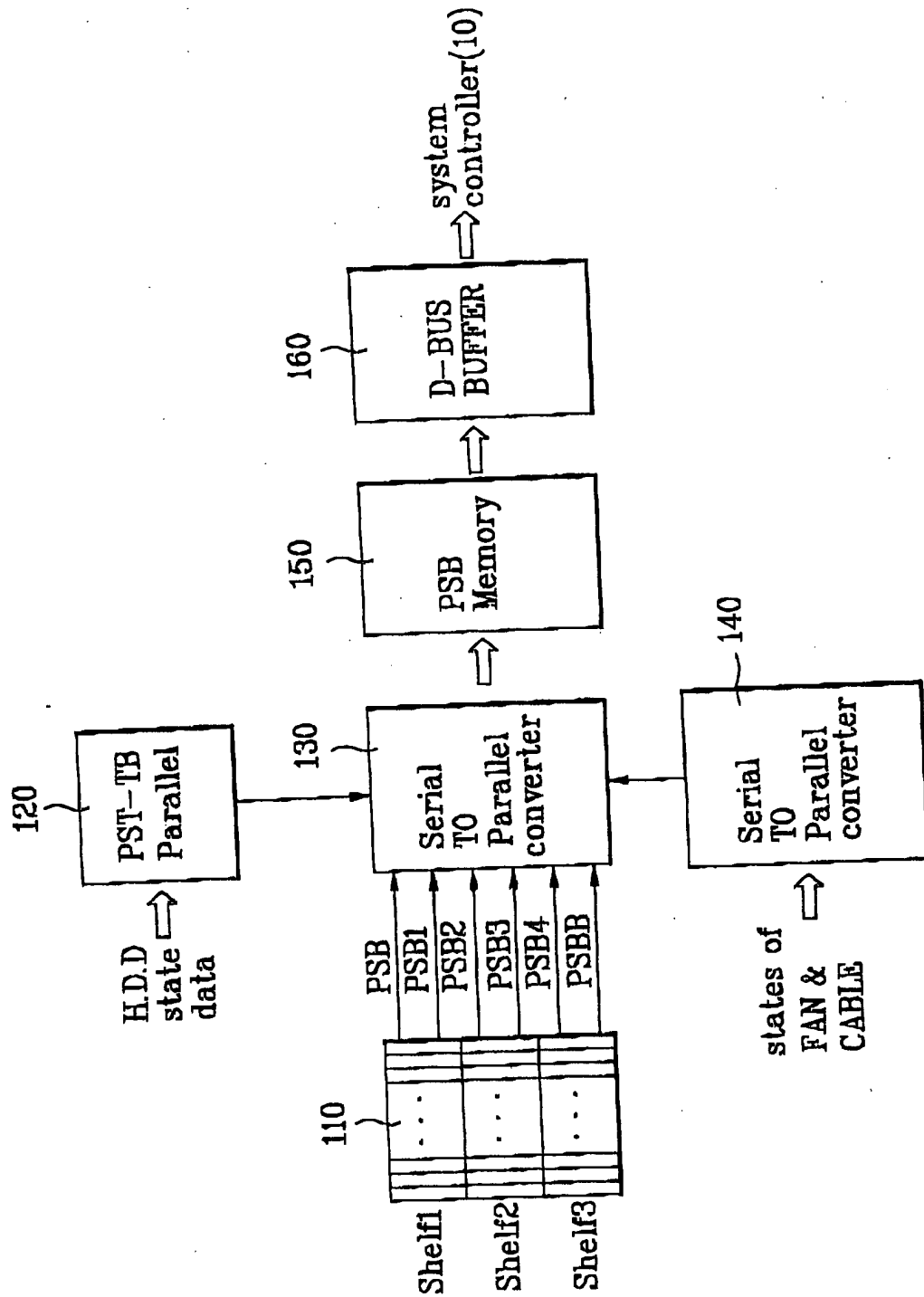




FIG. 3A

Add	b7~b4	b3~b0	Remarks
shelf— 0000 0001 0002 0003 0004 0005 0006 0007	DPM1 LC2 LC4 LC6 LC8 RDCC1 DKU3,4 PSU0,1	DPM1 LC1 LC3 LC5 LC7 RDCC0 (HUBC) DKU1,2 DKU5,6	DKU,PSU : PSB_TB conversion from PSB-TB to PSB
shelf— 0008 0009 000a 000b 000c 000d 000e 000f	DPM2 LC2 LC4 LC6 LC8 RDCC1 DKU3,4 PSU0,1	DPM2 LC1 LC3 LC5 LC7 RDCC0 (HUBC) DKU1,2 DKU5,6	
0010 0011 0012 0013 0014 0015 0016 0017	DPM3 LC2 LC4 LC6 LC8 RDCC1 DKU3,4 PSU0,1	DPM3 LC1 LC3 LC5 LC7 RDCC0 (HUBC) DKU1,2 DKU5,6	

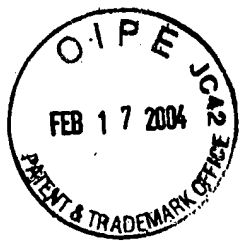
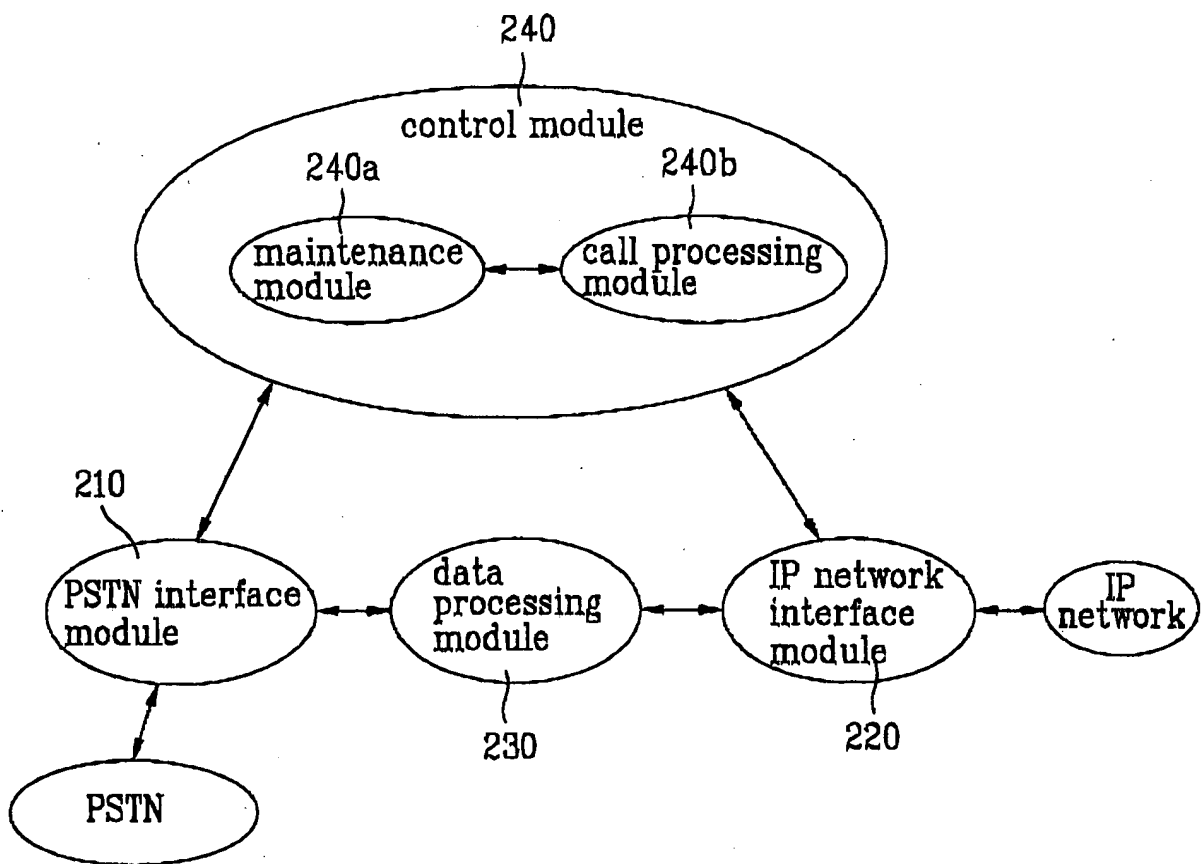


FIG. 3B

Add	b7~b4	b3~b0	Remarks
0018 0019 000a 000b 000c 000d 000e 000f	DPM4 LC2 LC4 LC6 LC8 RDCC1 DKU3,4 PSU0,1	DPM4 LC1 LC3 LC5 LC7 RDCC0 (HUBC) DKU1,2 DKU5,6	
0020 0021 0022 0023 0024 0025 0026 0027	CPM NIC2 NIC4 EIOC1 ASCC1 NSPC3 RDCC1 PSU0,1	CPM NIC1 NIC3 EIOC0 ASCC0 NSPC2 RDCC0 PSU1,2	DKU,PSU : PSB_TB conversion from PSB-TB to PSB

FIG. 4



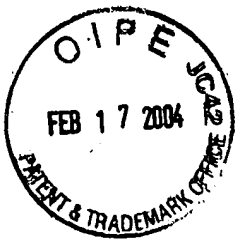


FIG. 5

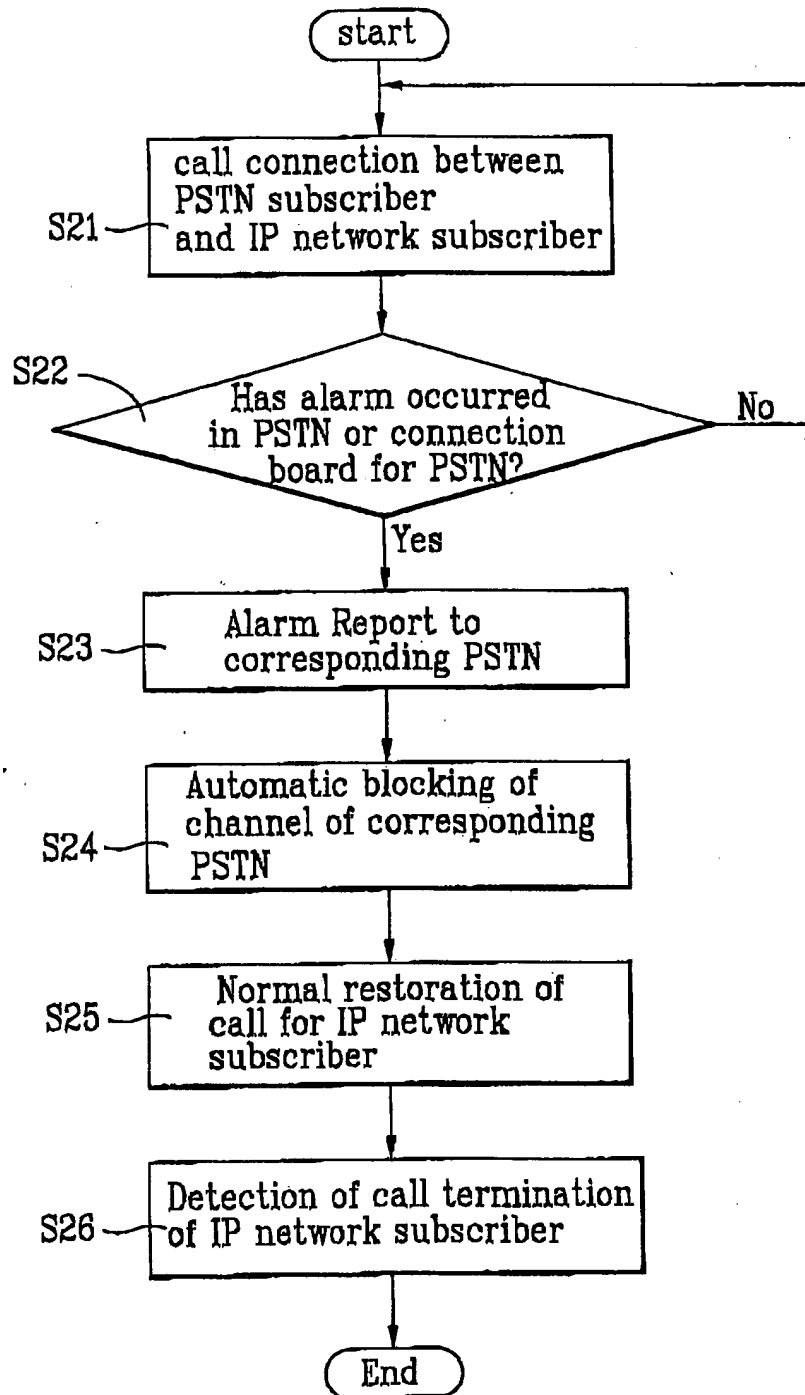


FIG. 6

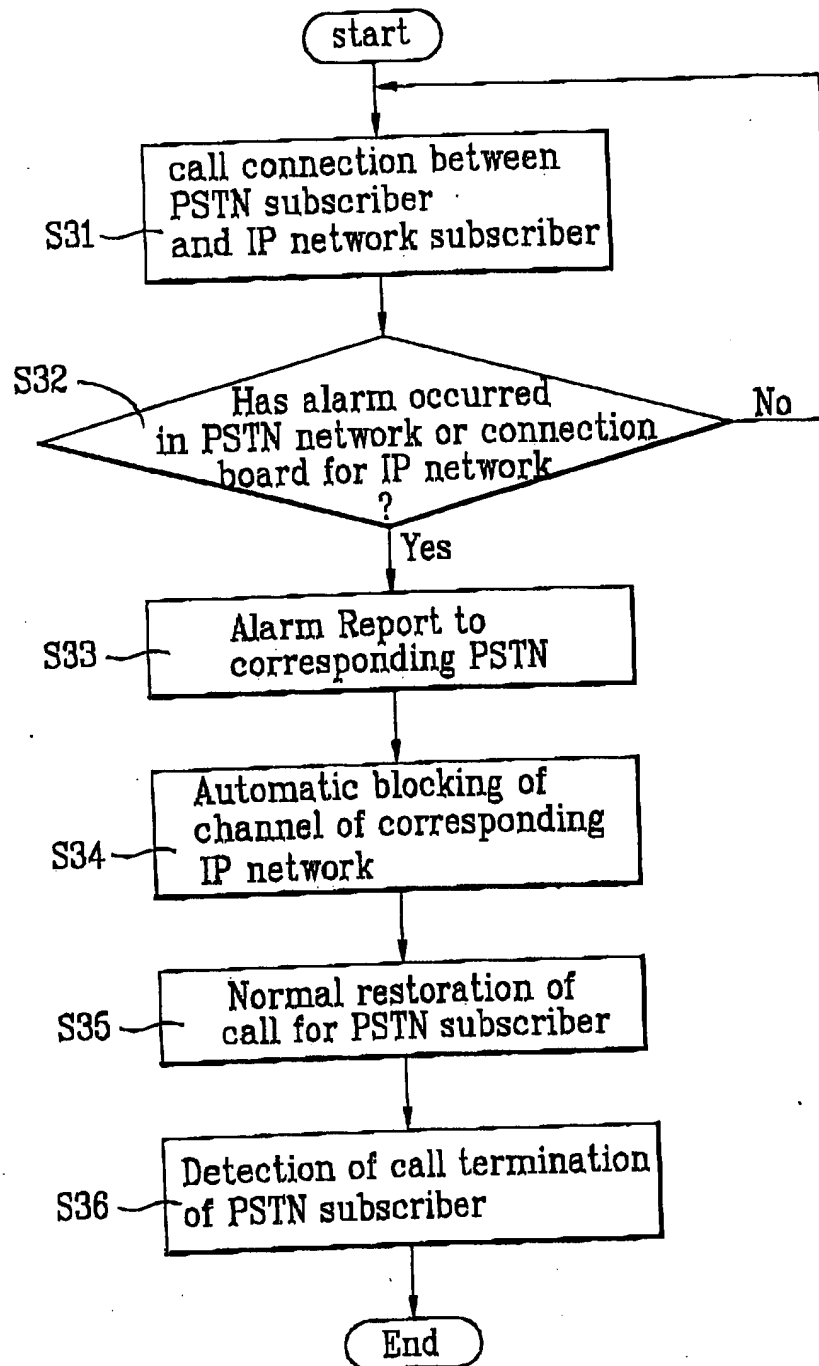




FIG. 7

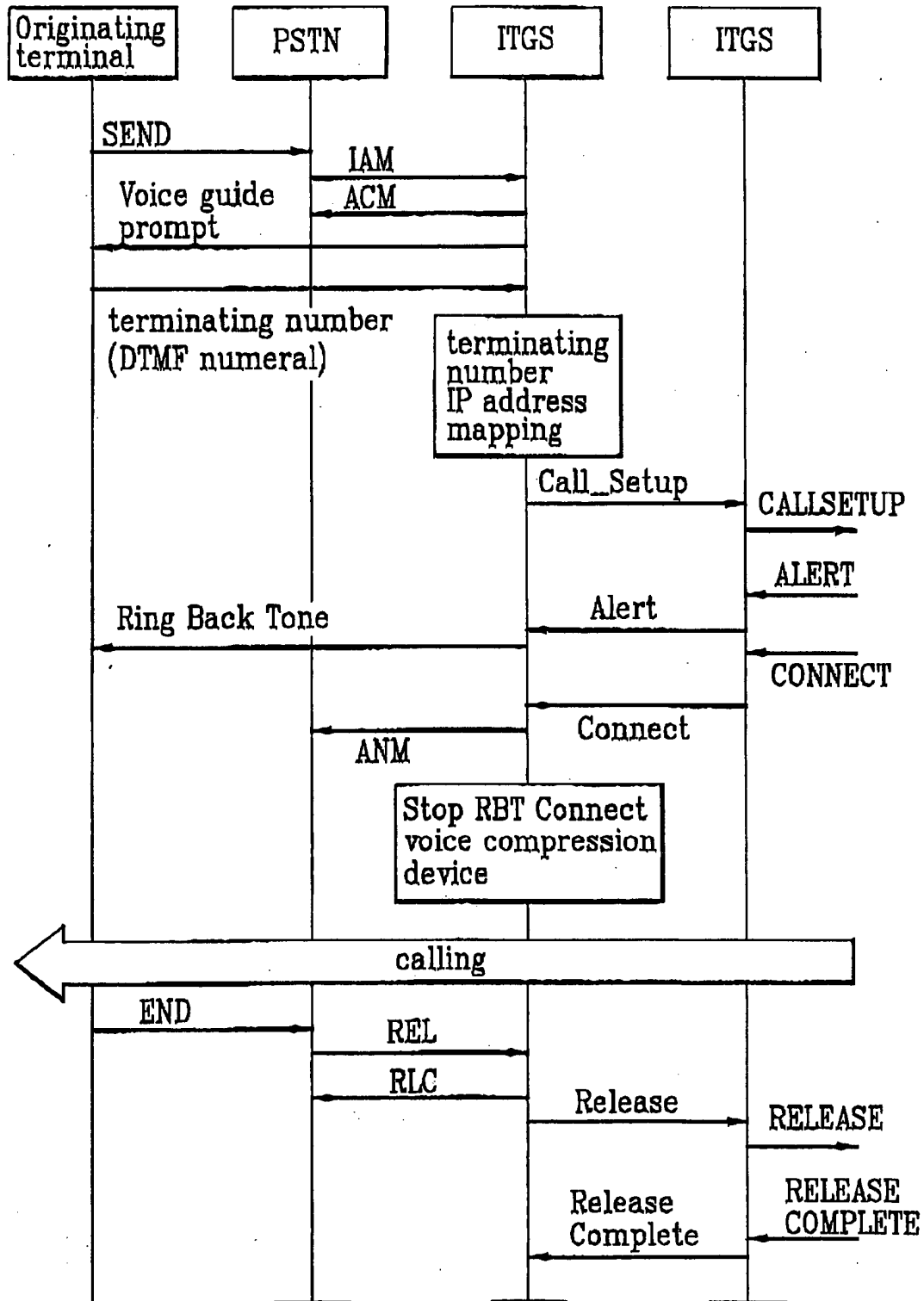




FIG. 8

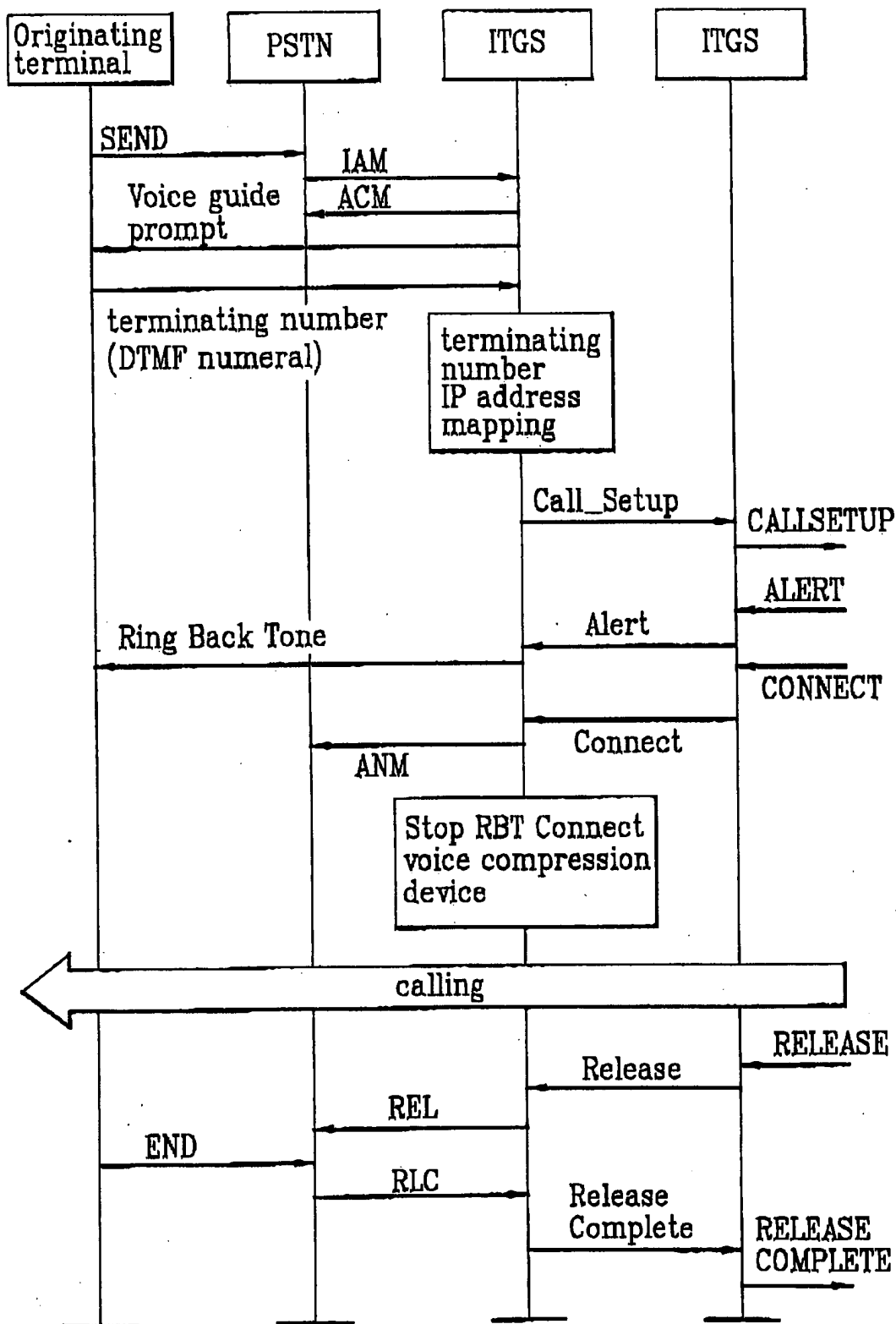


FIG. 9

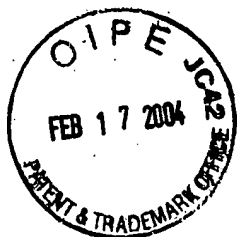
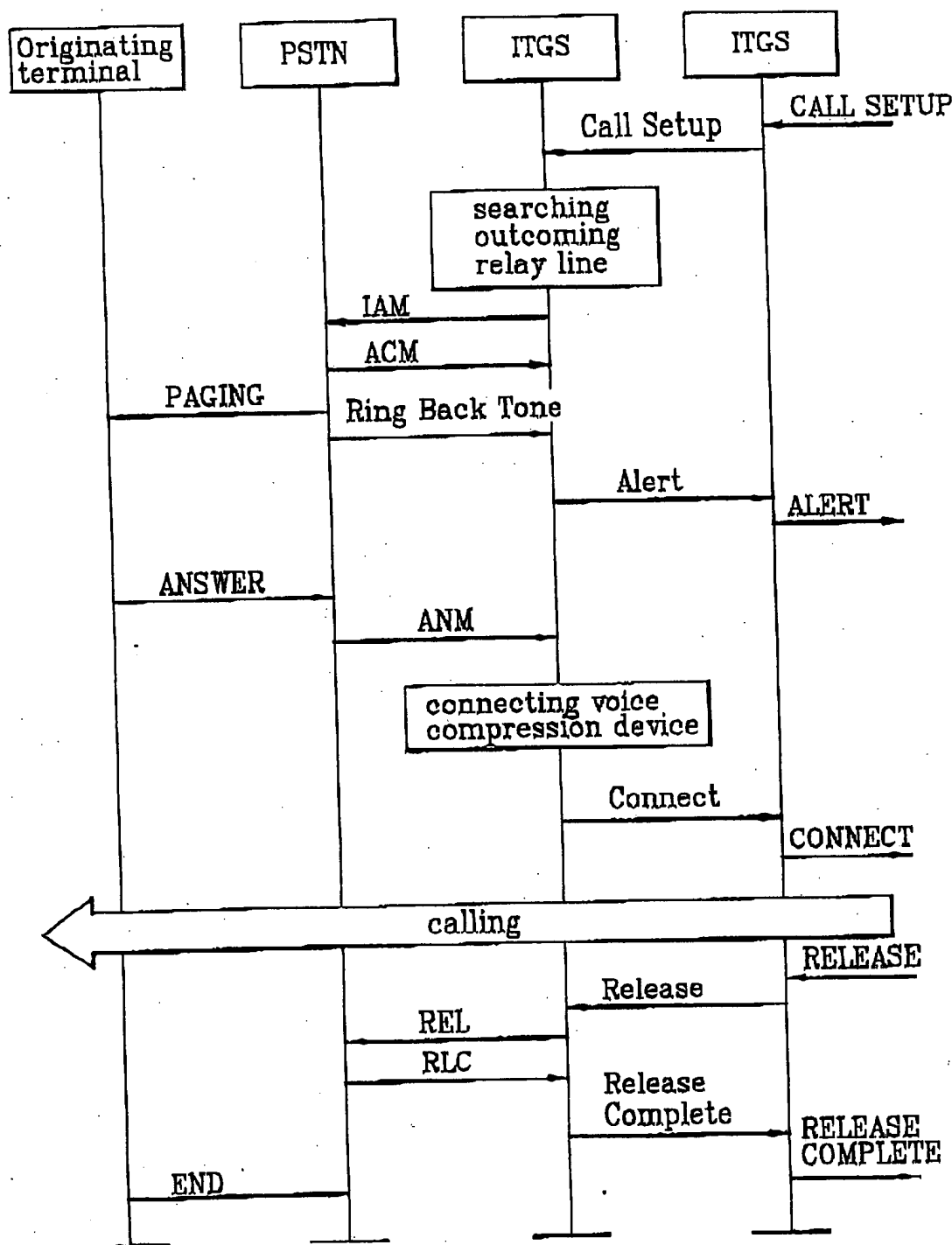


FIG. 10



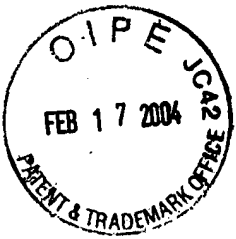


FIG. 11

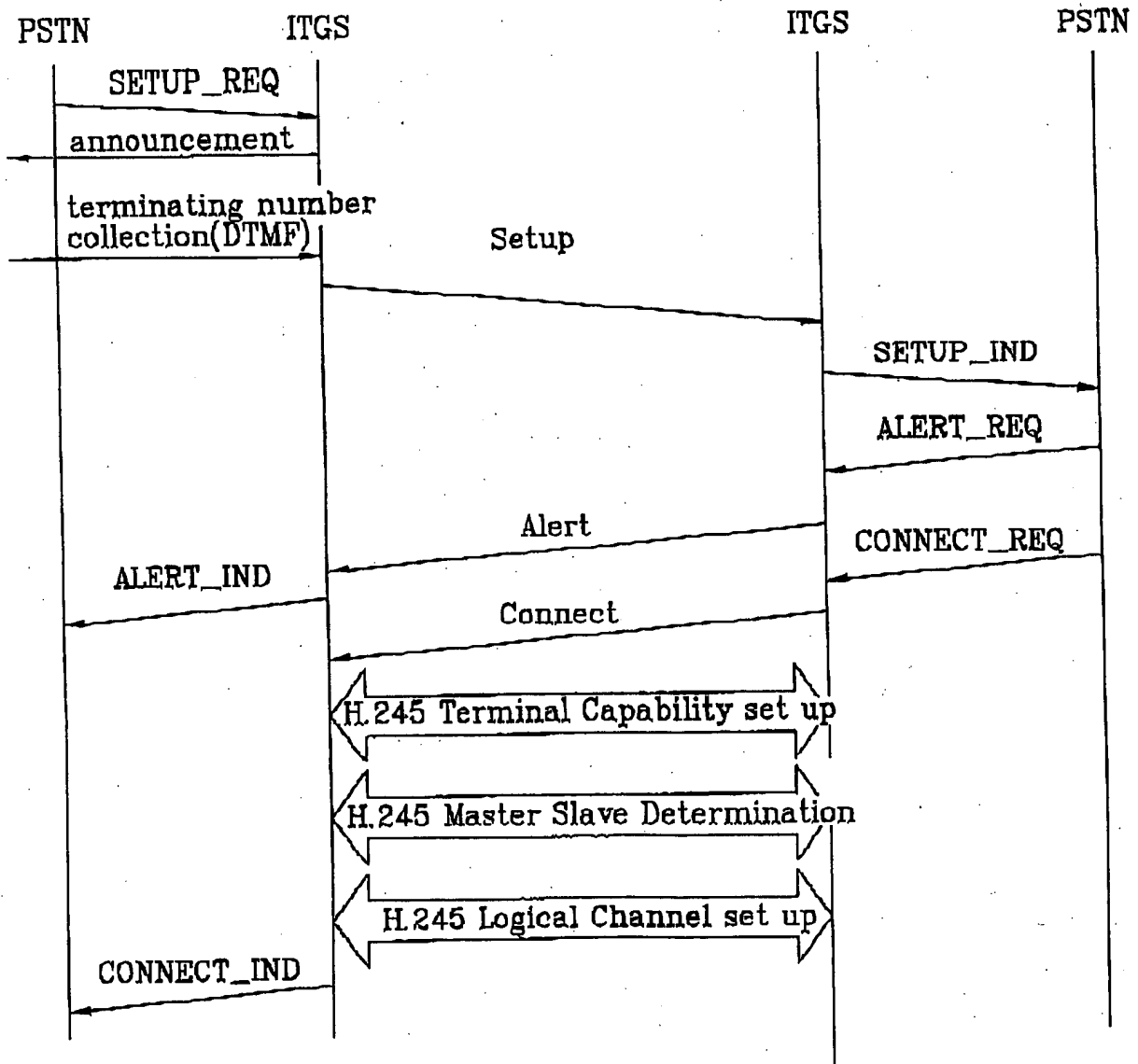




FIG. 12

